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Our Docket No.: 42390P17251

*Utility Patent Application***IN THE UNITED STATES PATENT AND TRADEMARK OFFICE**

In re the Patent Application of:

Tatiana Shpeisman

Serial No.: 10/748,384

Assignee: Intel Corporation

Filed: December 29, 2003

For: Data Layout Mechanism To Reduce
Hardware Resource Conflicts

Examiner: Wei, Zhang

Art Unit: 2194

RESPONSE TO OFFICE ACTIONMail Stop: Amendment
Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450**CERTIFICATE OF FACSIMILE TRANSMISSION**

I hereby certify that I am causing the above-referenced correspondence to be delivered to the USPTO via facsimile transmission to (571) 273-8300 and that this paper or fee has been addressed to Mail Stop Amendment, Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450.

Date of Deposit: December 21, 2006

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Signature

December 21, 2006
DateDocket No.: 42390P17251
Application No.: 10/748,384

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Utility Patent Application

INTRODUCTORY COMMENTS

Sir:

In response to the Office Action mailed September 14, 2006, Applicants respectfully request the Examiner to enter the following amendment and to consider the following Remarks.

Amendments to the Claims are reflected in the Listing of Claims, which begins on page 3 of this paper.

Remarks begin on page 14 of this paper.